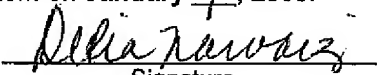
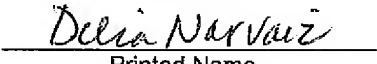


<b>IN THE UNITED STATES PATENT AND TRADEMARK OFFICE</b>	
<b>REPLY TO OFFICE ACTION DATED 10/14/2008</b>	Atty. Docket No. <b>SUZU1720</b>
Applicant <b>Takeshi Ishigaki</b>	
Application Number <b>10/586,951</b>	Date Filed <b>07/25/2006</b>
Title <b>Semiconductor Integrated Circuit Device and Method of Outputting Signals on Semiconductor Integrated Circuit</b>	
Group Art Unit <b>2816</b>	Examiner <b>Rojas, Daniel E.</b>
Confirmation Number: <b>3389</b>	

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

<b><u>Certificate of Transmission Under 37 C.F.R. § 1.8</u></b>
I hereby certify that this correspondence is being deposited electronically with the U.S. Patent and Trademark Office using the United States Patent and Trademark Office's EFS-Web system on <b>January 7, 2009</b> .
 Signature
 Printed Name

In response to the Official Action mailed October 14, 2008, Applicant respectfully requests the Examiner reconsider the rejections of the Claims in view of this reply.